

J1129
U.S. PATENT & TRADEMARK OFFICE
PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
Washington, D.C. 20231

Case Docket No.: 50P4300.01/1576

J1046 U.S. PTO
09/899436
07/05/01

Sir:

Transmitted herewith for filing is the patent application of

Applicant(s): Harry Chue and Garry Kak Tsang

Title: System And Method For Efficiently Implementing An Electronic Device Architecture

Enclosed are:

26 pages of specification, claims and abstract.
 8 sheets of [] informal formal drawing(s).
 A declaration and power of attorney.
 An assignment transmittal.
 An assignment of the invention to: Sony Corporation and Sony Electronics, Inc.
Please record the assignment and return to the undersigned.
 A certified copy of a _____ application.
 An associate power of attorney.
 A verified statement to establish small entity status under 37 CFR 1.9 and 1.27.
 PTO Form-1449 and copies of cited art.

The filing fee has been calculated as shown below:

For	(Col. 1) No. Filed	(Col. 2) No. Extra	Small Entity		or	Other Than a Small Entity	
			Rate	Fee		Rate	Fee
Basic Fee				\$385.00			\$710.00
Total Claims	43 - 20 =*	23	x \$11 =	\$		x \$18 =	\$414.00
Indep. Claims	4 - 3 = *	1	x \$40 =	\$		x \$80 =	\$80.00
Multiple Dependent Claims Present []			+ \$130 =	\$		+ \$260 =	\$
*If the difference in column 1 is less than zero, enter 0 in column 2			Total	\$	or	Total	\$1204.00

Please charge my Deposit Account No. 50-1443 the amount of \$ _____. A duplicate copy of this sheet is enclosed.

A check in the amount of \$ 1244.00 to cover the filing fee and recording of assignment is enclosed.

The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 50-1443. A duplicate copy of this sheet is enclosed.

Any additional filing fees required under 37 CFR 1.16.

Any patent application processing fees under 37 CFR 1.17.

The issue fee set in 37 CFR 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR 1.311(b).

Respectfully submitted,


Gregory J. Koerner, Registration No. 38,519
Simon & Koerner LLP
10052 Pasadena Avenue, Suite B
Cupertino, CA 95014
TEL: (408) 873-3943
FAX: (408) 873-3945

Dated: 7/31/01

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Chue et al.

SERIAL NO.: Unknown

FILING DATE: On Even Date Herewith

TITLE: System And Method For Efficiently Implementing
An Electronic Device Architecture

EXAMINER: Unknown

GROUP ART UNIT: Unknown

ATTY.DKT.NO.: 50P4300.01/1576

BOX PROVISIONAL APPLICATION
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WASHINGTON, D.C. 20231

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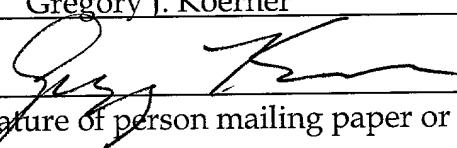
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